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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTORNEY DOCKET NO. P07472US00/WEJ

Date: 19 December 2001

DESIGNATED/ELE	U.S. APPLICATION NO.			
CONCERNING A FILING UNDER 35 U.S.C. 371				
INTERNATIONAL APPLICATION NO. INTERNATIONAL FILING DATE PRIORITY DATE CLAIMED 20 JUNE 2000 22 JUNE 1999				
TITLE OF INVENTION: EPITAXIAL F	TITLE OF INVENTION: EPITAXIAL FILMS			
APPLICANT(S) FOR DO/EO/US: TRAN		A		
Applicant herewith submits to the US Design	nated/Elected Office (DO/EO/US) the following	g items and other information:		
☐ 1. This is a FIRST submission of	items concerning a filing under 35 U.S.C.	371.		
2. This is a SECOND or SUBSE	QUENT submission of items concerning a	filing under 35 USC 371.		
examination until the expiration \(\times \) 4. A proper Demand for Internation	 3. This express request to begin national examination procedures (35 USC 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 USC 371(b) and PCT Art. 22 and 39(1). 4. A proper Demand for International Preliminary Examination was made by the 19th month from the earliest 			
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c. is not required, as the appli	ication was filed in the United States Recei	ving Office (RO/US).		
6. A translation of the Internation	nal Application into English (35 U.S.C. 37)	1(c)(2)).		
7. Amendments to the claims of the	ne International Appln. under PCT Article	19 (35 USC 371 (c)(3))		
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c. have not been made; howe d. have not been made and w	ver, the time limit for making such amendr	nents had NOT expired.		
	ts to the claims under PCT Article 19 (35 U	I.S.C. 271(-)(2))		
9. An oath or declaration of the in	expenter(s) $(35 \text{ U.S.C.} 371(c)(4))$	5.5.C. 3/1(c)(3)).		
10. A translation of the annexes to	the Int'l Prelim. Exam. Report under PCT	Article 36 (35 IJ S C 371(a)(5))		
Items 11. to 20. below concern do	cument(s) or information included:	Macie 30 (33 0.3.c. 371(c)(3)).		
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14. A Second or Subsequent preliminary amendment.				
A substitute specification.				
16. A change of power of attorney and/or address letter.				
17. A computer-readable form of the sequence listing in accordance with PCT Rule 13ter.2 & 35 USC 1.821-825.				
18. A second copy of the published international application under 35 USC 154(d)(4).				
 19. A second copy of the English translation of the international application under 35 USC 154(d)(4). 20. Other items or information: 				
A copy of the Notification of Missir	ng Requirements under 35 U.S.C. 371.			
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Applicant claims small entity status. See 37 CFR 1.27. The fees indicated above are reduced by ½.			\$ 520			
SUBTOTAL =			\$ 520			
Processing fee of \$130 for furnishing the English translation later than from the earliest claimed priority date (37 CFR 1.492(f)).			\$			
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1.137(a) or (b)) must be filed and granted to restore the application to pending status						
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent

In re patent application of: TRAN et al.

Serial No.: NEW APPLICATION

Filed: On even date herewith

For: EPITAXIAL FILM

Examiner:

Art Unit:

Dckt No.: P07472US00/WEJ

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents Washington, D.C.

SIR:

Prior to examination, please amend the above-identified application as follows.

IN THE CLAIMS:

A clean version of the amended claims is provided herewith in **Attachment A**. It will be noted that claims 1, 3, 4, 8 and 10-17 have been amended relative to the previously provided version as shown by the marked up version thereof in **Attachment B** provided herewith.

REMARKS

By this Amendment, the claims have been rewritten to reduce the multiple dependencies.

Further and favorable action is solicited.

Respectfully submitted,

Date: 12/19/01

Douglas E. Jackson Registration No. 28518

LARSON & TAYLOR PLC - 1199 North Fairfax Street, Suite 900 - Alexandria, Virginia 22314 -

ATTACHMENT A

Clean Replacement/New Claims (entire set of pending claims)

Following herewith is a clean copy of the entire set of pending claims.

- 1. (amended) An epitaxial zinc-based II-VI semiconductor film grown using single source chemical vapor deposition.
- 2. An epitaxial film as claimed in claim 1, wherein the epitaxial film comprises ZnS.
- 3. (amended) An epitaxial film as claimed in claim 2, wherein the ZnS is grown using zinc diethyldithiocarbamate as precursor for the single source chemical vapor deposition.
- 4. (amended) An epitaxial film as claimed in claim 2, wherein the ZnS is grown using Zn(S₂CRN₂)₂, where R comprises an alkyl group, as a precursor for the single source chemical vapor deposition.
- 5. A process as claimed in claim 4, wherein the number of carbon atoms in the alkyl group is in the range from 1 to 6.
- 6. A process comprising the steps of utilizing single source chemical vapor deposition for growing epitaxial zinc-based II-VI semiconductor film on a substrate.
- A process as claimed in claim 6, wherein the epitaxial film comprises ZnS.
- 8. (amended) A process as claimed in claim 7, wherein the process comprises the use of $Zn(S_2CNR_2)_2$, where R comprises an alkyl group, as precursor for the single source chemical vapor deposition.

- 9. A process as claimed in claim 8, wherein the number of carbon atoms in the alkyl group is in the range from 1 to 6.
- 10. (amended) A process as claimed in claim 7, wherein the process comprises the use of zinc diethyldithiocarbamate as a precursor for the single source chemical vapor deposition.
- 11. (amended) A process as claimed in claim 6, wherein the substrate comprises a silicon substrate.
- 12. (amended) A substrate coated with a coating comprising an eptiaxial zinc-based II-VI semiconductor film grown using single source chemical vapor deposition.
- 13. (amended) A substrate as claimed in claim 12, wherein the substrate comprises silicon.
- 14. (amended) A substrate as claimed in claim 12, wherein the epitaxial film comprises ZnS.
- 15. (amended) A process for growing an eptiaxail zinc-based II-VI semiconductor film, the process comprising the steps of:
 - cleaning a substrate,
 - heating the substrate to a deposition temperature,
 - the sublimation of a single source chemical vapor deposition precursor;
 - the pyrolysis of the precursor molecules on the heated substrate; and
 - the formation of the epitaxial film on the heat substrate.
- 16. (amended) A process as claimed in claim 15, wherein the substrate comprises silicon.

17. (amended) A process as claimed in claim 15, wherein the epitaxial film comprises ZnS.

ATTACHMENT B

Marked Up Replacement Claims

Following herewith is a marked up copy of each rewritten claim together with all other pending claims.

- 1. (amended) An epitaxial zinc-based II-VI semiconductor film grown using single source chemical vapourvapor deposition.
- 2. An epitaxial film as claimed in claim 1, wherein the epitaxial film comprises ZnS.
- 3. (amended) An epitaxial film as claimed in claim 2, wherein the ZnS is grown using zinc diethyldithiocarbamate as precursor for the single source chemical vapour vapor deposition.
- 4. (amended) An epitaxial film as claimed in claim 2, wherein the ZnS is grown using Zn(S₂CRN₂)₂, where R comprises an alkyl group, as a precursor for the single source chemical vapour deposition.
- 5. A process as claimed in claim 4, wherein the number of carbon atoms in the alkyl group is in the range from 1 to 6.
- 6. A process comprising the steps of <u>utilisingutilizing</u> single source chemical <u>vapourvapor</u> deposition for growing epitaxial zinc-based II-VI semiconductor film on a substrate.
- 7. A process as claimed in claim 6, wherein the epitaxial film comprises ZnS.
- 8. (amended) A process as claimed in claim 7, wherein the process comprises the use of $Zn(S_2CNR_2)_2$, where R comprises an alkyl group, as precursor for the single source chemical vapourvapor deposition.

- 9. A process as claimed in claim 8, wherein the number of carbon atoms in the alkyl group is in the range from 1 to 6.
- 10. (amended) A process as claimed in claim 7, wherein the process comprises the use of zinc diethyldithiocarbamate as a precursor for the single source chemical vapourvapor deposition.
- 11. (amended) A process as claimed in any one of claims 6 to 10 claim 6, wherein the substrate comprises a silicon (111) substrate.
- 12. (amended) A substrate coated with a coating comprising an eptiaxial zinc-based II-VI semiconductor film grown using single source chemical vapourvapor deposition.
- 13. (amended) A substrate as claimed in claim 12, wherein the substrate comprises silicon-(111).
- 14. (amended) A substrate as claimed in claims 12-to-13, wherein the epitaxial film comprises ZnS.
- 15. (amended) A process for growing an eptiaxail zinc-based II-VI semiconductor film, the process comprising the steps of:
- cleaning a substrate,
- heating the substrate to a deposition temperature,
- the sublimation of a single source chemical vapourvapor deposition precursor;
- the pyrolysis of the precursor molecules on the heated substrate; and
- the formation of the epitaxial film on the heated substrate.
- 16. (amended) A process as claimed in claim 15, wherein the substrate comprises silicon (111).

17. (amended) A process as claimed in claim 15 or 16, wherein the epitaxial film comprises ZnS.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent

In re patent application of: TRAN et al.

Serial No.: 10/018,401

Examiner:

Filed: December 19, 2001

Art Unit:

For: EPITAXIAL FILM

Dckt No.: P07472US00/WEJ

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents Washington, D.C.

SIR:

Prior to examination, please amend the above-identified application as follows.

IN THE CLAIMS:

A clean version of the all pending claims is provided herewith in **Attachment A.** It will be noted that claims 13 and 16 have been amended relative to the previously provided version as shown by the marked up version thereof in **Attachment B** provided herewith.

REMARKS

By this Amendment, the claims have been amended to correct errors in the previous Preliminary Amendment.

Further and favorable action is solicited.

Respectfully submitted,

Date: April 10, 2002

Registration No. 24,016

LARSON & TAYLOR PLC - 1199 North Fairfax Street, Suite 900 - Alexandria, Virginia 22314 -

ATTACHMENT A

Clean Replacement/New Claims

Following herewith is a clean copy of the amended claims.

- 13. (Thrice Amended) A substrate as claimed in claim 12, wherein the substrate comprises silicon (111).
- 16. (Thrice Amended) A process as claimed in claim 15, wherein the substrate comprises silicon (111).

ATTACHMENT B

Marked Up Replacement Claims

Following herewith is a marked up copy of each rewritten claim.

- 13. (Thrice Amended) A substrate as claimed in claim 12, wherein the substrate comprises (111) silicon (111).
- 16. (Thrice Amended) A process as claimed in claim 15, wherein the substrate comprises (111)-silicon (111).

PCT/AU00/00696

EPITAXIAL FILMS

10/018401

Field of the Invention

The present invention relates broadly to the growth of epitaxial II-VI semiconductor films. The invention will be described herein with reference to the growth of epitaxial zinc sulfide (ZnS) on silicon (Si) (111) substrates, but it will be appreciated that the invention does have broader applications relating to growth of epitaxial II-VI semiconductor films of different materials and/or on different substrates.

Background of the Invention

Films that grow with singular crystallographic orientation in all directions are referred to as epitaxial films. This has to be contrasted with poly-crystalline thin films, which include a large number of crystallites — but with variable orientations with respect to each other.

Epitaxial thin films have been produced using a variety of different techniques, including molecular beam epitaxy (MBE), vapour phase epitaxy (VPE) and atomic layer epitaxy (ALE). However, a common characteristic of those techniques is that the epitaxial film growth requires multiple sources for the film elements, for example separate sources for zinc (Zn) and sulphur (S) are required for the epitaxial growth of ZnS films. Therefore, such techniques can have the disadvantage of being rather complex processes, during which a large number of variables must be controlled. This often results in high costs associated with the operation of machines for epitaxial film growth.

30 Epitaxial thin films are desirable for a large number of applications including light emitting layers for diodes, as active layers in optical/electro-optical thin film devices and as coatings. In this application, the single-crystal like characteristics of epitaxial films are utilised, which are typically superior to the characteristics of polycrystalline films.

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Summary of the Invention

In accordance with a first aspect of the present invention there is provided an epitaxial zinc-based II-VI semiconductor film grown using single source chemical vapour deposition.

In one embodiment, the epitaxial film comprises ZnS.

Preferably, the ZnS is grown using zinc

diethyldithiocarbamate as precursor for the single source chemical vapour deposition.

In another preferred embodiment, the ZnS is grown using $\text{Zn}(S_2\text{CNR}_2)_2$, where R comprises an alkyl group, as a precursor for the single source chemical vapour deposition.

The number of carbon atoms in the alkyl group is preferably in the range from 1 to 6.

In accordance with a second aspect of the present invention there is provided a process comprising the steps of utilising single source chemical vapour deposition for growing an epitaxial zinc-based II-VI semiconductor film on a substrate.

In one embodiment, the epitaxial film comprises ZnS.

In one preferred embodiment the process comprises the use of zinc diethyldithiocarbamate as a precursor for the single source chemical vapour deposition.

In another preferred embodiment, the ZnS is grown using $Nz(S_2CNR_2)_2$, where R comprises an alkyl group, as a precursor for the single source chemical vapour deposition.

The number of carbon atoms in the alkyl group is preferably in the range from 1 to 6.

Preferably, the substrate comprises a silicon (111) 30 substrate.

In accordance with a third aspect of the present invention, there is provided a substrate coated with a coating comprising an epitaxial zinc-based II-VI semiconductor film grown using single source chemical vapour deposition.

Preferably, the substrate comprises silicon (111). In one embodiment, the epitaxial film comprises ZnS.

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In accordance with a fourth aspect of the present invention, there is provided a process for growing an epitaxial zinc-based II-VI semiconductor film, the process comprising the steps of cleaning a substrate, heating the substrate to a deposition temperature, the sublimation of a single source chemical vapour deposition precursor;

the pyrolysis of the precursor molecules on the heated substrate; and

the formation of the epitaxial film on the heated 10 substrate.

Preferably, the substrate comprises silicon (111).

In one embodiment, the epitaxial film comprises ZnS. Preferred forms of the invention will now be described, by way of example only, with reference to the accompanying drawings.

Brief Description of the Drawings

Figure 1 is a schematic drawing of a deposition chamber embodying the present invention.

Figure 2 shows angle dependent X-ray photoelectron defraction measurements of epitaxial films embodying the present invention.

Figure 3 is schematic drawing illustrating a side view of a ZnS crystalline structure.

Figure 4 shows an X-ray photoelectron spectroscopy wide scan of a ZnS film embodying the present invention.

Figure 5 shows an angle dependent X-ray photoelectron defraction measurements of a ZnS film after sputtering.

Figure 6 shows energy dependent X-ray photoelectron defraction measurements of an epitaxial film embodying the present invention.

Figure 7 is schematic drawing illustrating a side view of a ZnS crystalline structure.

Figure 8 is a schematic drawing illustrating the formation of an epitaxial film embodying the present invention.

AMENDED SHEET

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Figure 9 is a block diagram illustrating the growth of epitaxial films embodying the present invention.

Figure 10 is a schematic diagram illustrating a device application embodying the present invention.

5 Detailed Description of the Preferred Embodiments

In Figure 1, a high vacuum deposition chamber 10 (base pressure 10⁻⁷ Torr) comprises a resistively heated Knudsen cell 12 loaded with a zinc diethyldithiocarbamate precursor powder (not shown) for the single source chemical vapour deposition (SSCVD). A silicon Si(111) substrate 19 is mounted on a sample holder 16 on a heater 100 and the epitaxial film (not shown) is formed on the substrate 19. The chamber 10 further comprises a view port 11, a port 13 to which a vacuum pump (not shown) is connected and a flexible flange 15 as part of a x,y,z manipulator 17 for the heater 100.

As illustrated in Figure 8, sublimed zinc diethyldithiocarbamate molecules 80 impinge on the heated substrate 19. In the diethyldithiocarbamate molecules 80, the zinc atom is in a similar environment to that of zinc in crystalline ZnS. The SSCVD growth of the ZnS epitaxial film 84 proceeds via the pyrolysis of $\text{Zn}[S_2\text{CN}(C_2\text{H}_5)_2]_2$ on the heated substrate 19 (400°C):

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$$\operatorname{Zn}[S_2\operatorname{CN}(C_2H_5)_2]_2 \rightarrow \operatorname{ZnS} + C_2H_5\operatorname{NCS} + (C_2H_5)_3\operatorname{NCS}_2$$
 (1)

 C_2H_5NCS and $(C_2H_5)_3NCS_2$ decompose into by-products such as C_2H_4 , CS_2 and $(C_2H_5)NH$ which are volatile in vacuum and therefore do not remain on the heated substrate 19 during the ZnS epitaxial film growth.

In this embodiment epitaxial film growth of ZnS was found on the Si (111) surface (lattice mismatch $\sim 0.2\%$).

As shown in Figure 9, in one embodiment the growth of epitaxial films comprises the cleaning of the Si substrate (step 90), the heating of the Si substrate (step 92), the sublimation of the diethyldithiocarbamate precursor (step

94), the pyrolysis of the diethyldithiocarbamate molecules on the heated substrate (step 96) and the formation of the epitaxial ZnS film on the heated substrate (step 98).

The cleaning of the Si(111) substrates (step 90) in one embodiment comprises the sequence of steps outlined in Table 1.

1	annealing in oxygen	1050°C	30 min
2	rinse in deionised ${ m H}_2{ m O}$	room temp	5 min
		(ultrasonic bath)	
3	rinse in EtOH	room temp	5 min
		(ultrasonic bath)	
4	rinse in Iso-propyl	room temp	5 min
	alcohol	(ultrasonic bath)	
5	N ₂ blown dry		30 sec
6	$12H_2O$: $7NHF_4$: $1HF$	room temp	10 min
7	rinse in deionised ${ m H}_2{ m O}$	room temp	1 min
8	N ₂ blown dry		30 sec
9	5H ₂ O : 1HCl : 1H ₂ O ₂	80°C, oil bath	10 min
10	rinse in deionised ${ m H}_2{ m O}$	room temp	1 min
11	N_2 blown dry		30 sec
12	$12H_2O$: $7NHF_4$: $1HF$	room temp	10 min
13	rinse in deionised ${ m H}_2{ m O}$	room temp	1 min
14	N ₂ blown dry		30 sec
15	$5H_2O$: $1HCl$: $1H_2O_2$	80°C, oil bath	10 min
16	rinse in deionised ${\rm H_2O}$	room temp	1 min
17	N_2 blown dry		30 sec
18	12H ₂ O : 7NHF ₄ : 1HF	room temp	10 min
19	rinse in deionised ${ m H}_2{ m O}$	room temp	1 min
20	N_2 blown dry		30 sec
21	$5H_2O$: $1HCl$: $1H_2O_2$	80°C, oil bath	10 min
22	rinse in deionised H_2O	room temp	1 min
23	N ₂ blown dry		30 sec
24	NH ₄ F (40%) or HF (5%)	room temp	10 min

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25	rinse in absolute EtOH room temp 2 min	
26	Mounting onto sample holder/heater 16	
27	loading into deposition chamber 10	
28	heating for removing 350°C, vacuum (10 ⁻⁸ torr) surface contaminants	1 5
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In will be appreciated, however, that other cleaning step sequences and different treatment times may be applied, which may e.g. comprise sputtering and annealing steps in the high vacuum deposition chamber 10 (Figure 1). Film Characterisation

The resulting epitaxial films were characterised using X-ray photoelectron spectroscopy (XPS) and X-ray photoelectron diffraction (XPD).

10 Figure 2 shows an angle dependent XPD scan of the Zn $2p_{3/2}$ intensity distribution for ZnS epitaxial films at thicknesses ranging from ~5 to 2000Å. The film thicknesses were estimated using the intensity attenuating of the XPS Si substrate peaks. The XPD measurements were performed 15 after subsequent SSCVD deposition cycles.

The XPD patterns exhibit an intense and broad peaks 20, 22, and 24 at θ =0° which are the result of forwardscattering of Zn $2p_{3/2}$ photoelectrons by neighbouring atoms. In ZnS, every zinc atom is surrounded by four sulfur atoms in a tetrahedral arrangement which results in either a cubic (sphalerite) or a, slightly distorted, hexagonal (wurtzite) structure.

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The enhanced XPD intensities 20, 22, 24 at θ =0° in curves a, b, and c of Figure 2 respectively therefore indicate that the film molecules have preferred orientation at the film-to-substrate interface and the Zn $2p_{3/2}$ photoelectrons are scattered by the sulfur neighbours perpendicular to the substrate.

As illustrated in Figure 3, the forward scattering enhancement 20, 22, 24 at θ =0° in curves a, b and c of Figure 2 is likely the result of forward-scattering of Zn2p_{3/2} photoelectrons emitted from the zinc atoms 30 at the sulphur atoms 32, which are positioned directly above the zinc atoms 30 at a distance of 2.3 Å in an ideal ZnS cubic crystal structure.

In Figure 4, a XPS wide scan 40 for a typical ZnS epitaxial film embodying the present invention is shown. In the curve 40 shown in Figure 4, the silicon substrate peaks can also be observed, which are not fully attenuated due to the thinness of the ZnS epitaxial film on which the XPS measurement shown in Figure 4 was performed. The chemical composition obtained from XPS scans such as the one shown in Figure 4 were in agreement with those obtained for a ZnS reference sample.

In Figure 5, the curve 50 shows the XPD measurement for the 2000Å thick film of curve c of Figure 2 after ${\rm Ar}^+$ ion etching.

During the Ar † ion etching, highly energetic (2000 electron Volt (2keV)) impact on the film surface, resulting in a disordering of the crystallographic structure of the surface. In curve 50 of Figure 5, the XPD scan therefore does not indicate a significant forward scattering enhancement at θ =0°.

Energy dependent XPD was employed to probe the inplane orientation of the film molecules. The sample position and angle remained unchanged while the energy of the incoming X-rays was varied.

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The energy dependent XPD features shown in Figure 6 are a result of diffraction of S 2p photoelectrons in the ZnS atomic network. The photoelectron take-off angle was 19° with respect to the surface plane, i.e. the measurement was sensitive for crystallographic order within the plane of the substrate. As diffraction is a long range order process (diffraction of photoelectrons requires single crystalline surfaces) the observation of peaks 60, 62, 64 demonstrates that the film is of epitaxial quality.

As illustrated in Figure 7, the peaks 60, 62, 64 in the measurement shown in Figure 6 are due to the forward scattering of S 2p photoelectrons emitted from the sulphur atoms 70 at zinc atoms 72, which are the next neighbours of the sulphur atoms 70 in the [111] crystallographic 15 direction 74, along which the measurement shown in Figure 6 was measured. The distance between the sulfur atoms 60 and the Zn atoms 72 in an ideal ZnS cubic crystal structure is 2.3 Å.

In the following, a specific device application embodying the present invention will be described with reference to Figure 10.

Silicon is transparent at the typical telecommunications wavelength and it has been shown that Silicon-On-Insulator (SOI) structures can be used as waveguides. In these structures the wave is guided by a thin silicon layer on SiO2.

Figure 10 illustrates the principles of an optical modulator design 100. The silicon 102 is partially replaced by an epitaxial ZnS layer 104 which acts as waveguide. A suitable AC voltage applied across the ZnS layer 104 alters the refractive index of the ZnS and it is therefore possible to modulate light 105 guided through the film directly.

The epitaxial ZnS layer 104 is grown on the remaining 35 slightly doped (111) oriented silicon layers 106 which also comprises the bottom electrode. A thin metal film 108

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(e.g. Cr) is deposited onto the ZnS film 104 and form the top electrode. Optical losses are dependent on the density of defects in the ZnS layer 104 and it is therefore of significant advantage that they can be grown single crystalline using the technology of the present invention.

It will be appreciated by a person skilled in the art that the present invention is not limited to that specific application, but other applications are possible, including for example in other optical modulator devices, optical waveguide devices, transistor and diode devices, blue light emitting devices, solar cells, and as coatings for infrared sensing, emitting, or transmitting devices.

In the claims that follow and in the summary of the invention, except where the context requires otherwise due to express language or necessary implication, the word "comprising" is used in the sense of "including", i.e. the features specified may be associated with further features in various embodiments of the invention.

In the claims that follow and in the summary of the invention, except where the context requires otherwise due to express language or necessary implication, the word "comprising" is used in the sense of "including", i.e. the features specified may be associated with further features in various embodiments of the invention.

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THE CLAIMS DEFINING THE INVENTION ARE AS FOLLOWS:

- 1. An epitaxial zinc-based II-VI semiconductor film grown using single source chemical vapour deposition.
- 2. An epitaxial film as claimed in claim 1, wherein the epitaxial film comprises ZnS.
 - 3. An epitaxial film as claimed in claim 2, wherein the ZnS is grown using zinc diethyldithiocarbamate as precursor for the single source chemical vapour deposition.
- 4. An epitaxial film as claimed in claim 2, wherein the ZnS is grown using $Zn(S_2CNR_2)_2$, where R comprises an alkyl group, as a precursor for the single source chemical vapour deposition.
 - 5. A process as claimed in claim 4, wherein the number of carbon atoms in the alkyl group is in the range from 1 to 6.
 - 6. A process comprising the steps of utilising single source chemical vapour deposition for growing an epitaxial zinc-based II-VI semiconductor film on a substrate.
- 7. A process as claimed in claim 6, wherein the epitaxial film comprises ZnS.
 - 8. A process as claimed in claim 7, wherein the process comprises the use of $\text{Zn}(S_2\text{CNR}_2)_2$, where R comprises an alkyl group, as a precursor for the single source chemical vapour deposition.
 - 9. A process as claimed in claim 8, wherein the number of carbon atoms in the alkyl group is in the range from 1 to 6.
- 10. A process as claimed in claim 7, wherein the
 30 process comprises the use of zinc diethyldithiocarbamate as
 a precursor for the single source chemical vapour
 deposition.
 - 11. A process as claimed in any one of claims 6 to 10, wherein the substrate comprises a silicon (111) substrate.

- 12. A substrate coated with a coating comprising an epitaxial zinc-based II-VI semiconductor film grown using single source chemical vapour deposition.
- 13. A substrate as claimed in claim 12, wherein the 5 substrate comprises silicon (111).
 - 14. A substrate as claimed in claims 12 or 13, wherein the epitaxial film comprises ZnS.
 - 15. A process for growing an epitaxial zinc-based II-VI semiconductor film, the process comprising the steps of:
- 10 cleaning a substrate,
 - heating the substrate to a deposition temperature,
 - the sublimation of a single source chemical vapour deposition precursor;
- the pyrolysis of the precursor molecules on the 15 heated substrate; and
 - the formation of the epitaxial film on the heated substrate.
 - 16. A process as claimed in claim 15, wherein the substrate comprises silicon (111).
- 17. A process as claimed in claim 15 or 16, wherein the epitaxial film comprises ZnS.

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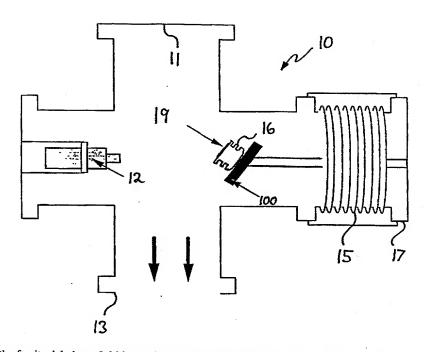
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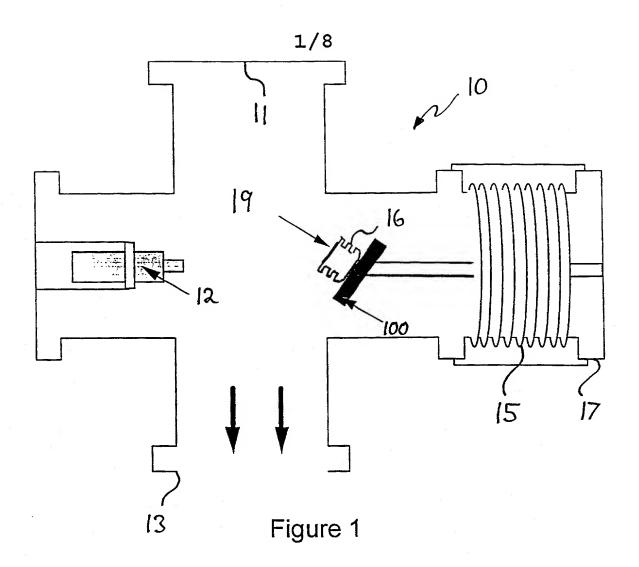
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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

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(57) Abstract: Growth of epitaxial zinc sulphide semiconductor film using zinc diethyldithiocarbamate precursor as single source.



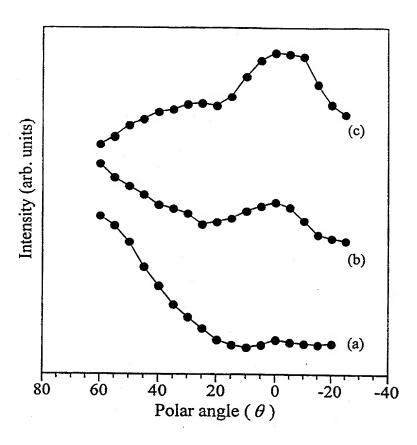


Figure 2

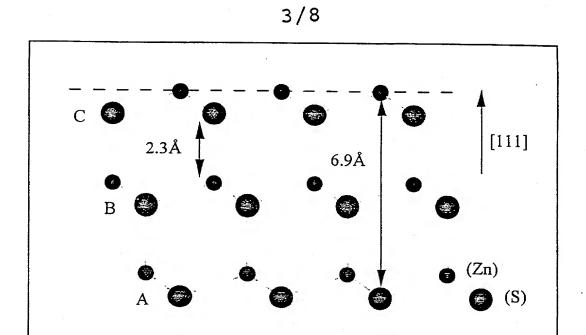


Figure 3

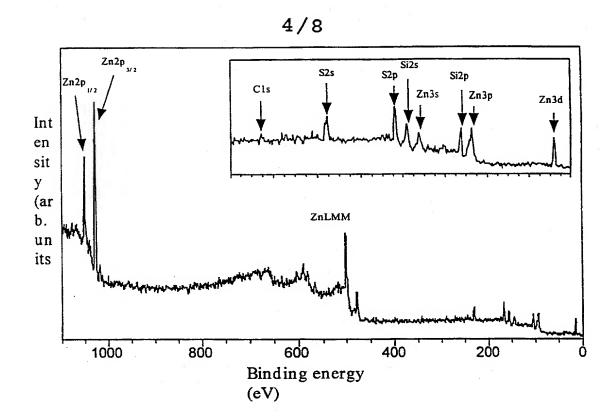


Figure 4

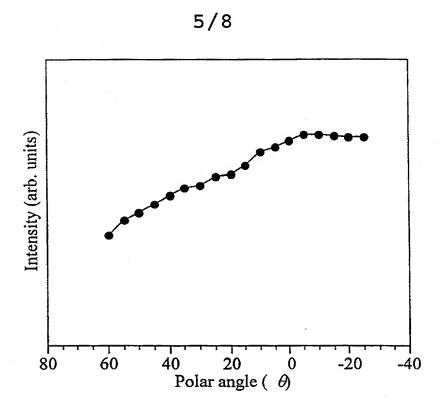


Figure 5

PCT/AU00/00696

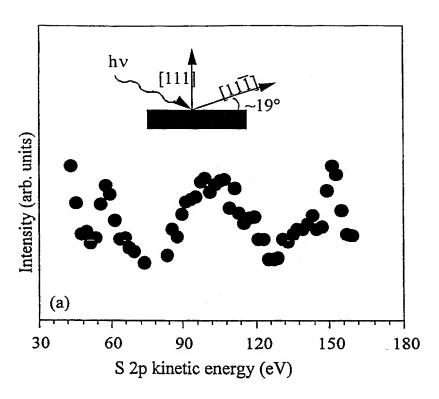


Figure 6

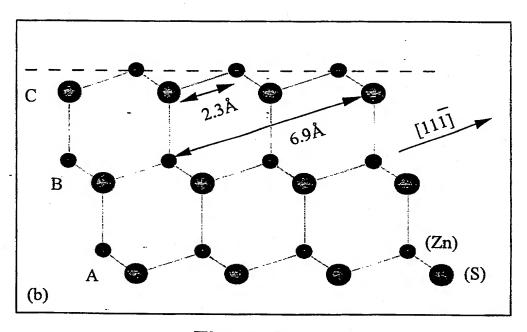


Figure 7

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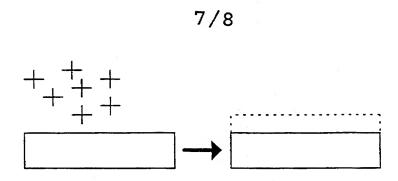


Figure 8

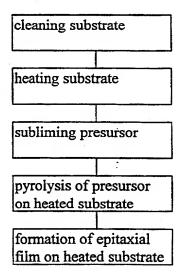


Figure 9

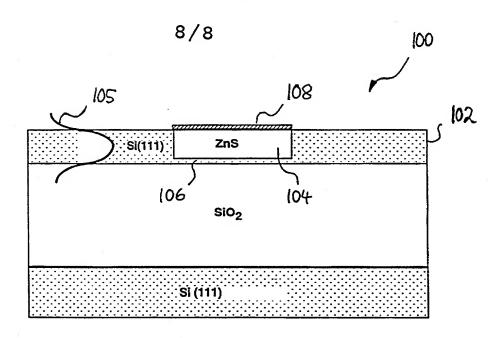


Figure 10

Customized PTO/SB/01 (10-01)

DECLARATION FOR UTILITY	Docket No. P07472US00/WEJ
OR DESIGN	1 st Inventor TRAN, Nguyen H.
PATENT APPLICATION	COMPLETE IF KNOWN
Declaration Submitted with Initial Filing	Serial No. 10/018,401
x Declaration Submitted after Initial Filing	Filing Date 19 December 2001

As a below named inventor, I hereby declare that: My residence, mailing address and citizenship are as stated below next to my name. I believe I am the original and first inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled:			
the specification of which: is attached hereto			
OR	as PCT International Application No. PC1	7/ALI00/00696	9
and (if applicable) was ame	ended on .		
amendment specifically referred to about	understand the contents of the above-ide		
Lacknowledge the duty to disclose info	mation which is material to patentability a	as defined in 37 CFR 1.56, inclu	ding for continuation-in-part
filing date of the continuation-in-part ap	became available between the filing dat plication.		
Lhereby claim EOREIGN PRIORITY be	enefits under 35 USC 119(a)-(d) or (f), or	365(b) of any foreign application	n(s) for patent, inventor's
certificate(s), or 365(a) of any PCT inte	rnat'l application which designated at leasiny foreign application for patent, inventor	st one country other than the Us s certificate(s) or any PCT intel	nat'l application having a filing
date before that of the application on w	hich priority is claimed. (ADDITIONAL A	APPLICATIONS IDENTIFIED ON AL	ODITIONAL INFORMATION SHEET)
Prior Foreign Appl. No.	Country	Day/Month/Year Filed	Priority Not Claimed
PQ1121	AU	22 JUNE 1999	
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I hereby declare that all statements ma	de herein of my own knowledge are true	and that all statements made o	n information and belief are
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punishable by fine or imprisonment, or	both, under 18 USC 1001 and that such in. (ADDITIONAL INVENTORS IDENTIFI	willful false statements may jeo ED ON ADDITIONAL INFORMATIO	pardize the validity of the
application or any patent issued thereo		ED ON ADDITIONAL IN CIVILATIO	A A .)
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